

Electronic Acknowledgement Receipt

EFS ID:	1248706
Application Number:	10523666
Confirmation Number:	8446
Title of Invention:	Circuit and method for controlling the threshold voltage of transistors
First Named Inventor:	Jose De Jesus Pineda De Gyvez
Customer Number:	24737
Filer:	Michael E. Marion/GiGi Lamprecht
Filer Authorized By:	Michael E. Marion
Attorney Docket Number:	NL 020835
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Application Type:	U.S. National Stage under 35 USC 371
International Application Number:	

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		NL020835US_RESP_TO_N ON-COMPLIANCE_12_OCT _2006.pdf	132626	yes	8

	Multipart Description		
	Doc Desc	Start	End
	Amendment - After Non-Final Rejection	1	1
	Claims	2	7
	Applicant Arguments/Remarks Made in an Amendment	8	8

Warnings:

Information:

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.